

Fig. 1 (prior art)

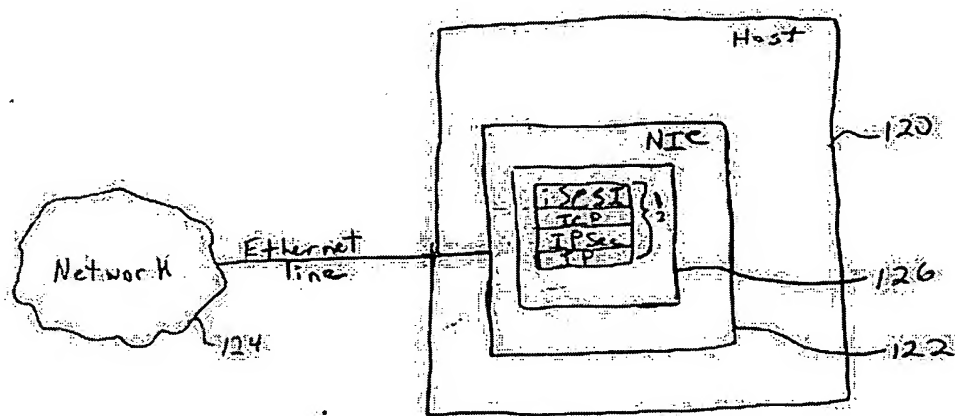


Fig. 2

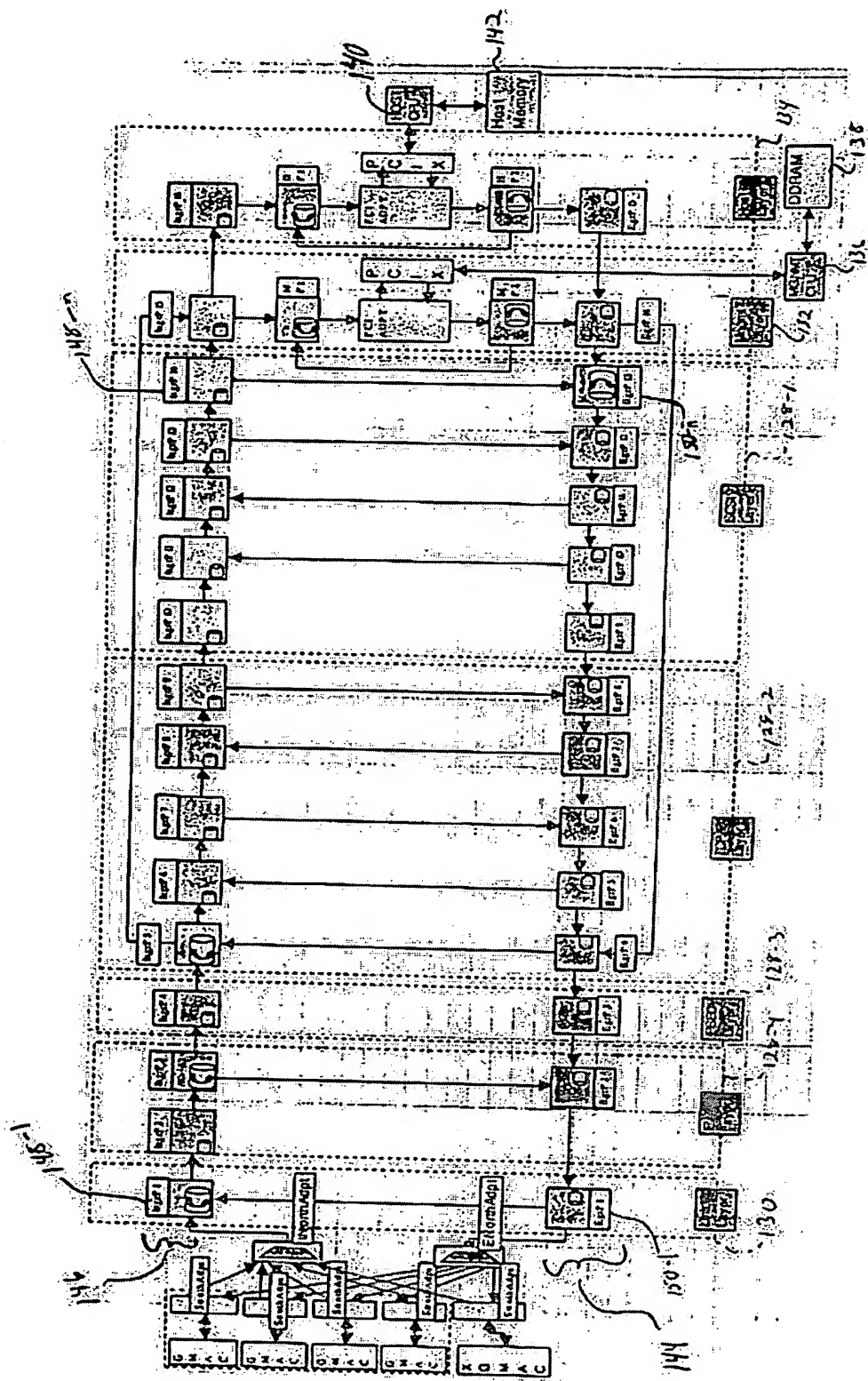
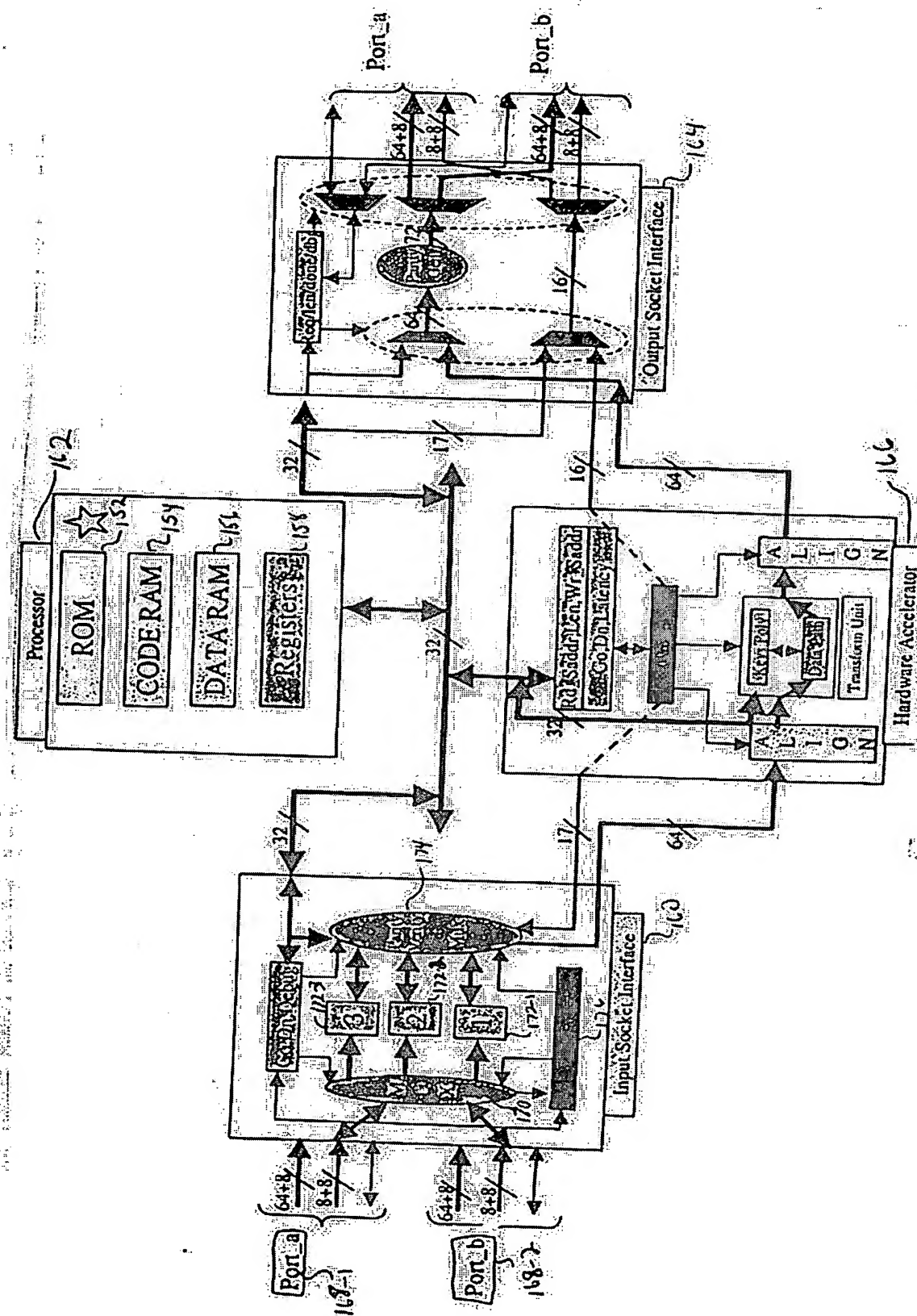


Fig. 3



FIA.

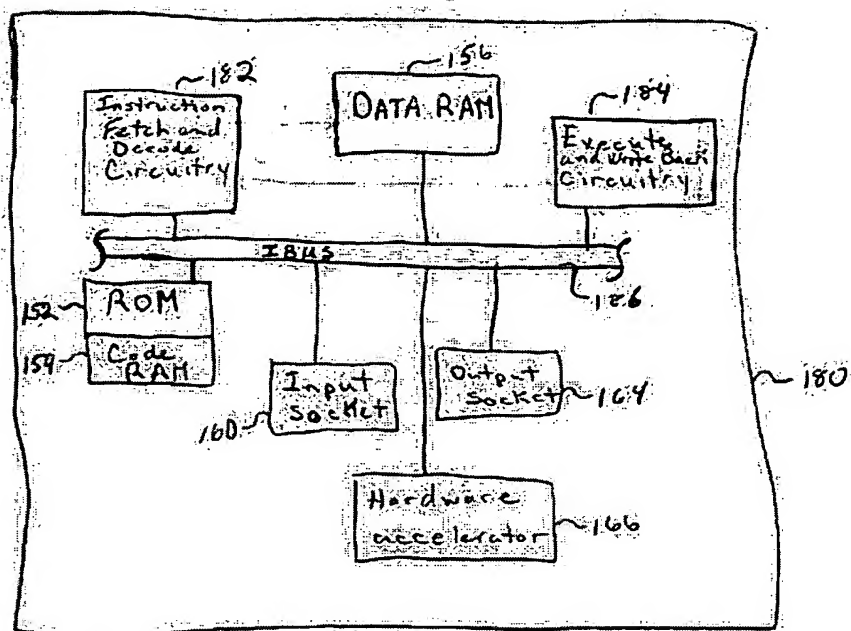


Fig. 5

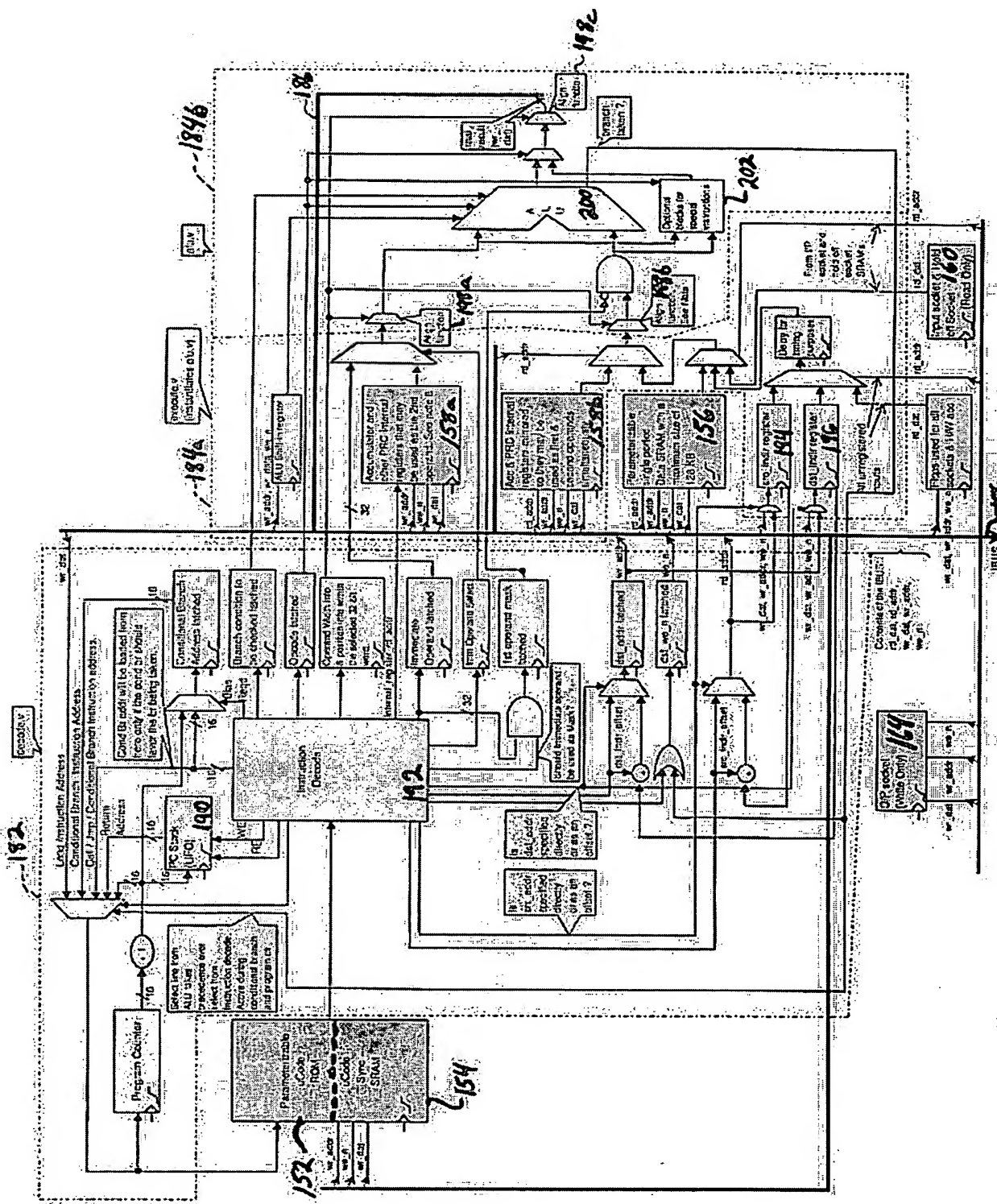
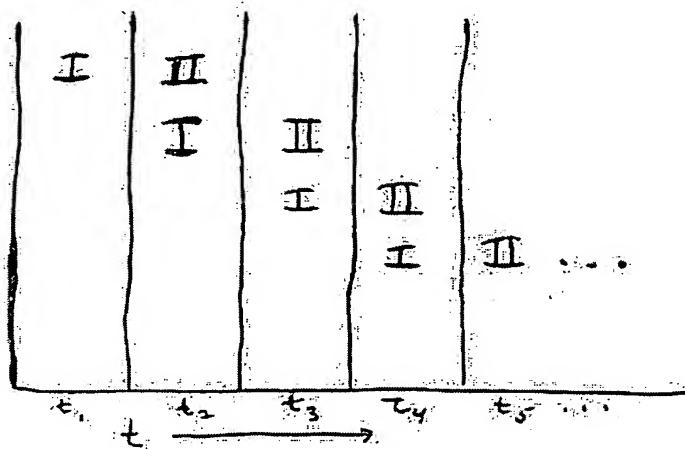


Fig. 6

I - instruction fetch and decode
II - execute and write back



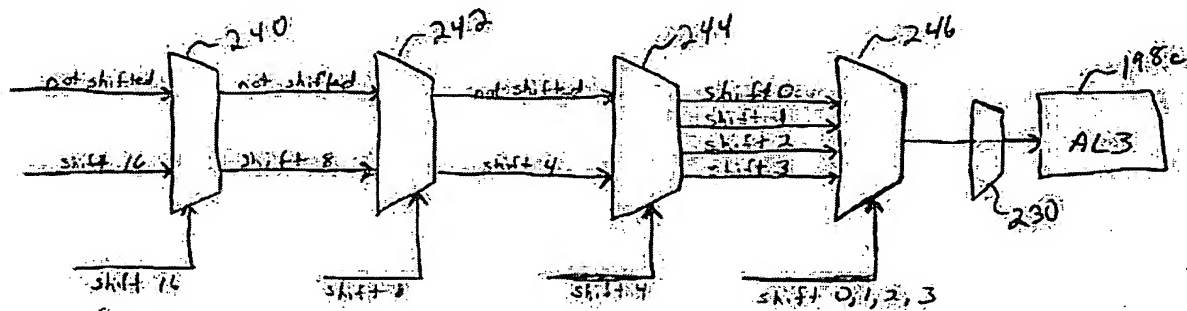


Fig. 9

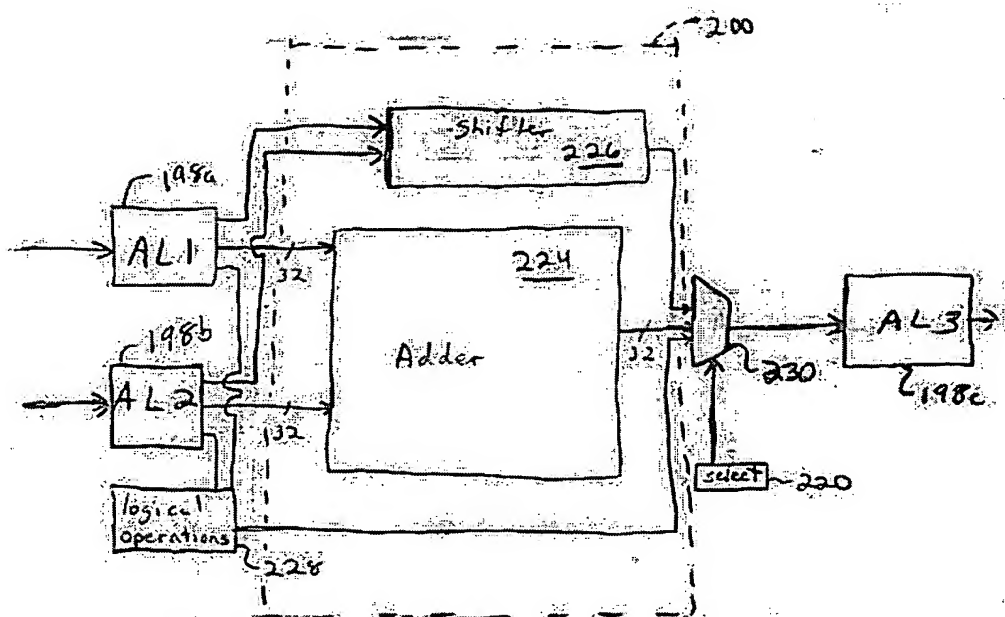


Fig. 8